



SamHop Microelectronics Corp.



STM4820

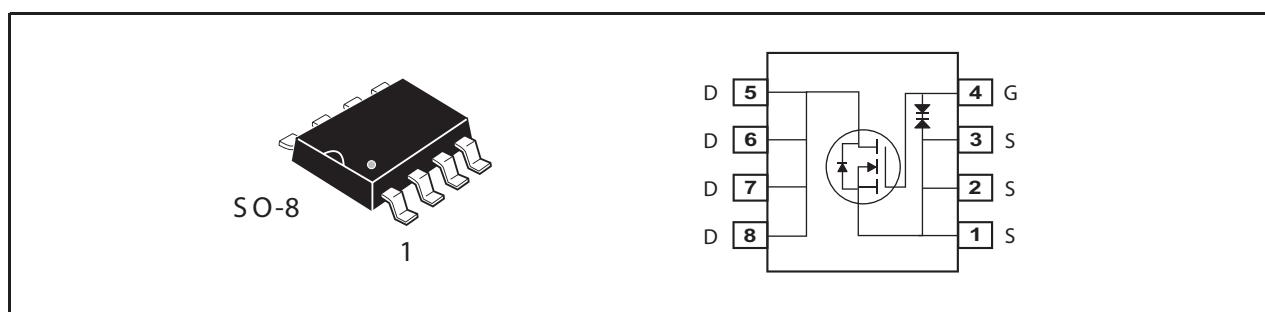
Ver 1.0

N-Channel Enhancement Mode Field Effect Transistor

| PRODUCT SUMMARY | | |
|------------------|----------------|----------------------------|
| V _{DSS} | I _D | R _{DSON} (mΩ) Max |
| 30V | 8.9A | 21 @ V _{GS} =10V |
| | | 30 @ V _{GS} =4.5V |

FEATURES

- Super high dense cell design for low R_{DSON}.
- Rugged and reliable.
- Surface Mount Package.
- ESD Protected.



ABSOLUTE MAXIMUM RATINGS (T_A=25°C unless otherwise noted)

| Symbol | Parameter | | Limit | Units |
|-----------------------------------|--|----------------------|------------|-------|
| V _{DS} | Drain-Source Voltage | | 30 | V |
| V _{GS} | Gate-Source Voltage | | ±20 | V |
| I _D | Drain Current-Continuous ^a | T _A =25°C | 8.9 | A |
| | | T _A =70°C | 7.1 | A |
| I _{DM} | -Pulsed ^b | | 45 | A |
| E _{AS} | Sigle Pulse Avalanche Energy ^d | | 10 | mJ |
| P _D | Maximum Power Dissipation ^a | T _A =25°C | 2.5 | W |
| | | T _A =70°C | 1.6 | W |
| T _J , T _{STG} | Operating Junction and Storage Temperature Range | | -55 to 150 | °C |

THERMAL CHARACTERISTICS

| | | | |
|------------------|--|----|------|
| R _{θJA} | Thermal Resistance, Junction-to-Ambient ^a | 50 | °C/W |
|------------------|--|----|------|

Details are subject to change without notice.

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ELECTRICAL CHARACTERISTICS (TA=25°C unless otherwise noted)

| Symbol | Parameter | Conditions | Min | Typ | Max | Units |
|--|---|---|-----|------|-----|-------|
| OFF CHARACTERISTICS | | | | | | |
| BVDSS | Drain-Source Breakdown Voltage | VGS=0V , ID=250uA | 30 | | | V |
| IBSS | Zero Gate Voltage Drain Current | VDS=24V , VGS=0V | | | 1 | uA |
| IGSS | Gate-Body Leakage Current | VGS= ±20V , VDS=0V | | | ±10 | uA |
| ON CHARACTERISTICS | | | | | | |
| VGS(th) | Gate Threshold Voltage | VDS=VGS , ID=250uA | 1 | 1.7 | 3 | V |
| RDS(ON) | Drain-Source On-State Resistance | VGS=10V , ID=8.9A | | 17 | 21 | m ohm |
| | | VGS=4.5V , ID=7.5A | | 23 | 30 | m ohm |
| gFS | Forward Transconductance | VDS=10V , ID=8.9A | | 14 | | S |
| DYNAMIC CHARACTERISTICS ^c | | | | | | |
| Ciss | Input Capacitance | VDS=15V,VGS=0V f=1.0MHz | | 460 | | pF |
| Coss | Output Capacitance | | | 135 | | pF |
| CRSS | Reverse Transfer Capacitance | | | 75 | | pF |
| SWITCHING CHARACTERISTICS ^c | | | | | | |
| tD(ON) | Turn-On Delay Time | VDD=15V ID=1A VGS=10V RGEN=6 ohm | | 8 | | ns |
| tr | Rise Time | | | 12 | | ns |
| tD(OFF) | Turn-Off Delay Time | | | 19 | | ns |
| tf | Fall Time | | | 26 | | ns |
| Qg | Total Gate Charge | VDS=15V, ID=8.9A, VGS=10V | | 7.6 | | nC |
| | | VDS=15V, ID=8.9A, VGS=4.5V | | 3.8 | | nC |
| Qgs | Gate-Source Charge | VDS=15V, ID=8.9A, VGS=10V | | 1.2 | | nC |
| Qgd | Gate-Drain Charge | | | 3.6 | | nC |
| DRAIN-SOURCE DIODE CHARACTERISTICS AND MAXIMUM RATINGS | | | | | | |
| Is | Maximum Continuous Drain-Source Diode Forward Current | | | | 2.0 | A |
| VSD | Diode Forward Voltage | VGS=0V, Is=2.0A | | 0.78 | 1.3 | V |
| Notes | | | | | | |
| a.Surface Mounted on FR4 Board,t ≤ 10sec. | | | | | | |
| b.Pulse Test:Pulse Width ≤ 300us, Duty Cycle ≤ 2%. | | | | | | |
| c.Guaranteed by design, not subject to production testing. | | | | | | |
| d.Starting TJ=25°C,L=0.5mH,RG=25Ω,VDD=20V,VGS=10V.(See Figure13) | | | | | | |

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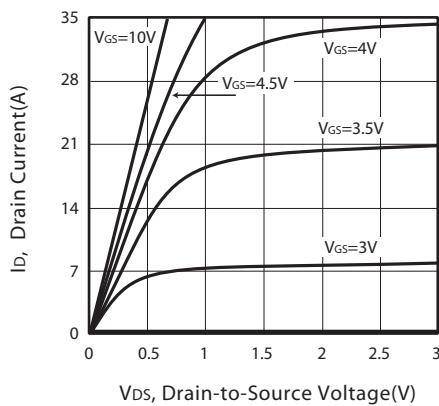


Figure 1. Output Characteristics

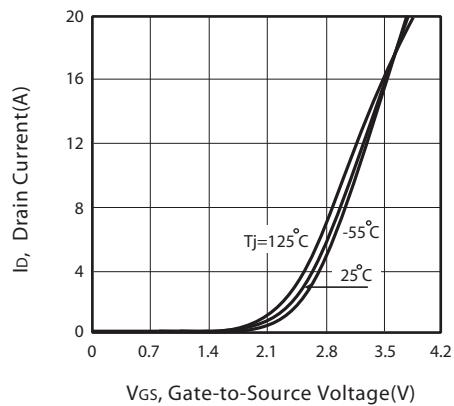


Figure 2. Transfer Characteristics

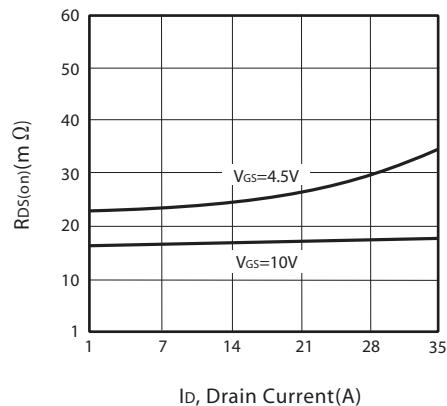


Figure 3. On-Resistance vs. Drain Current and Gate Voltage

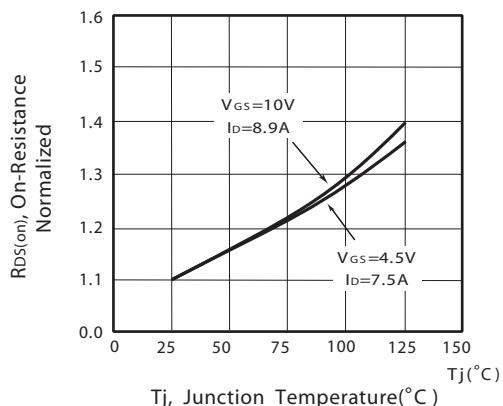


Figure 4. On-Resistance Variation with Drain Current and Temperature

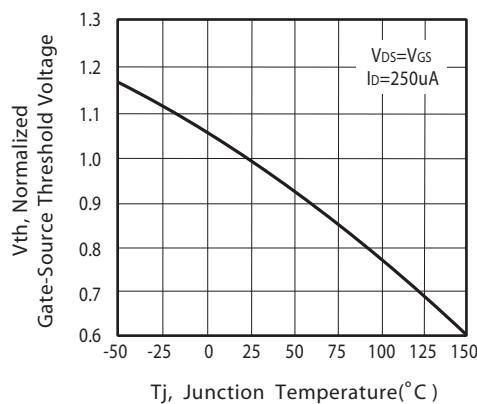


Figure 5. Gate Threshold Variation with Temperature

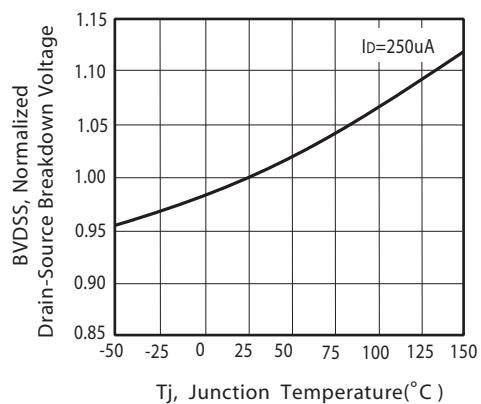
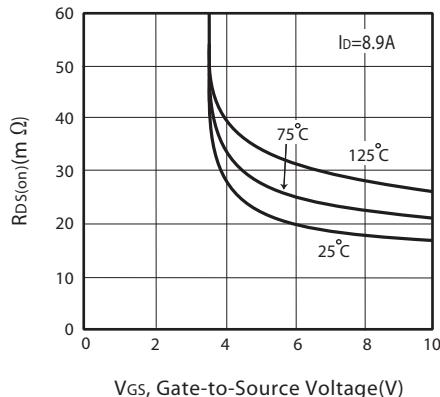


Figure 6. Breakdown Voltage Variation with Temperature

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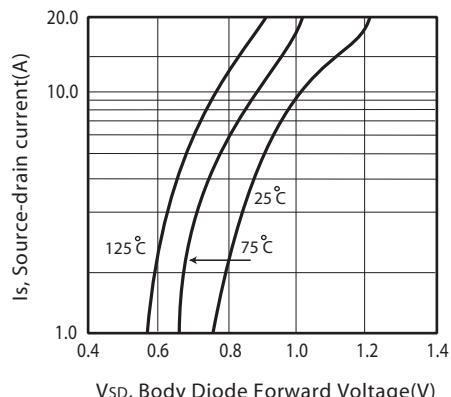
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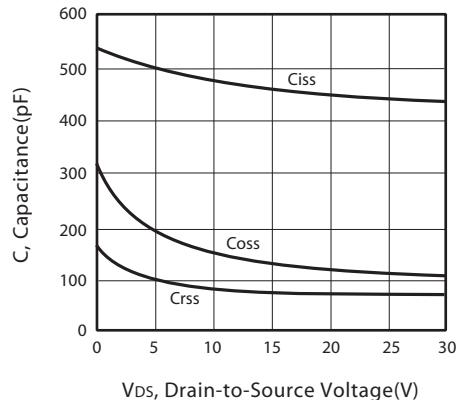
V_{GS} , Gate-to-Source Voltage(V)

Figure 7. On-Resistance vs.
Gate-Source Voltage



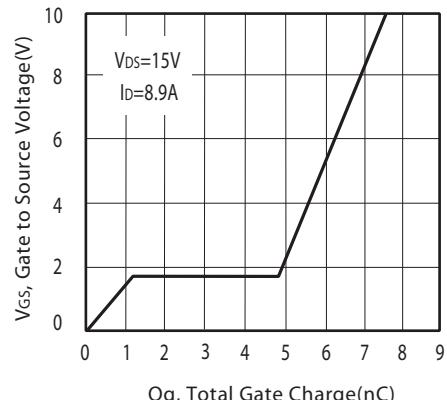
V_{SD} , Body Diode Forward Voltage(V)

Figure 8. Body Diode Forward Voltage
Variation with Source Current



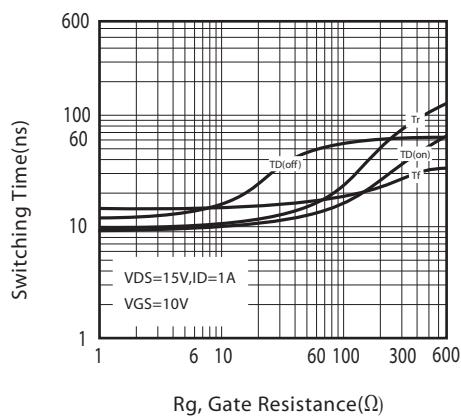
V_{DS} , Drain-to-Source Voltage(V)

Figure 9. Capacitance



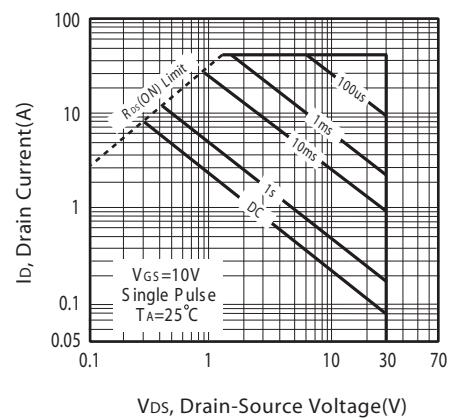
Q_g , Total Gate Charge(nC)

Figure 10. Gate Charge



R_g , Gate Resistance(Ω)

Figure 11. switching characteristics

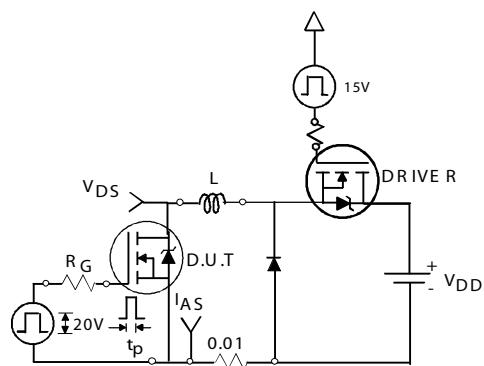


V_{DS} , Drain-Source Voltage(V)

Figure 12. Maximum Safe Operating Area

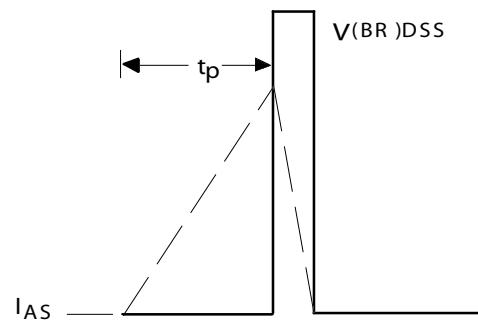
STM420

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Unclamped Inductive Test Circuit

Figure 13a.



Unclamped Inductive Waveforms

Figure 13b.

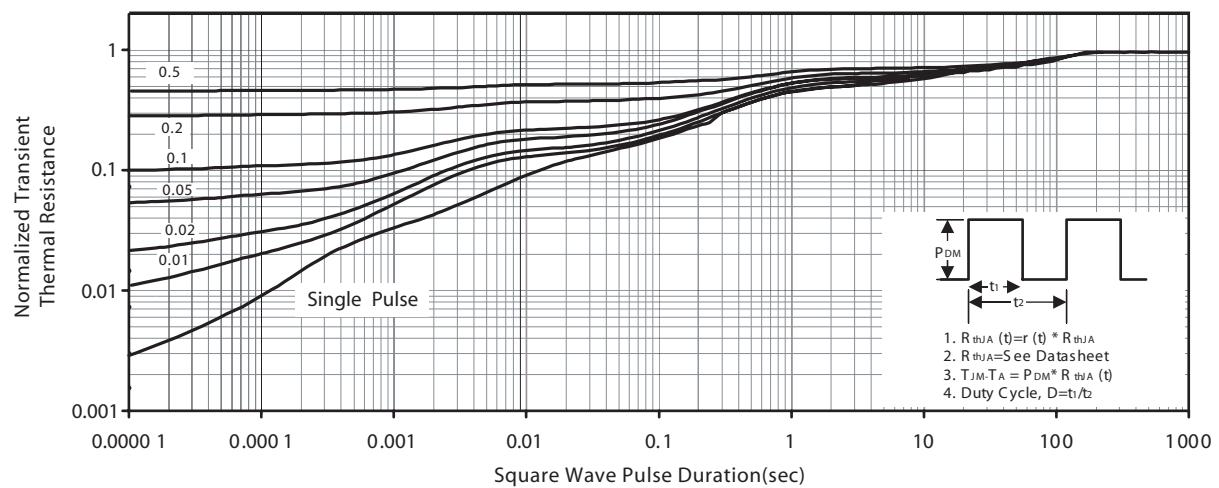
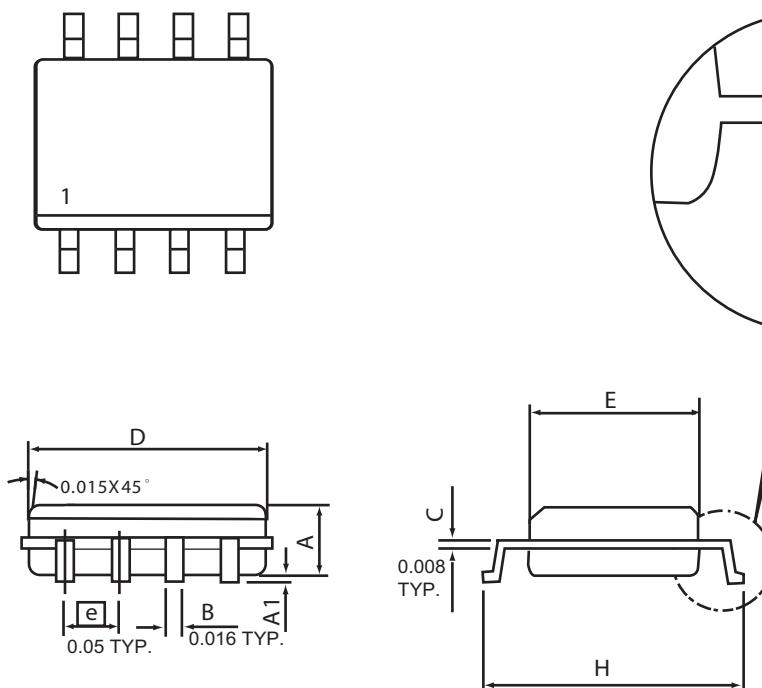


Figure 14. Normalized Thermal Transient Impedance Curve

PACKAGE OUTLINE DIMENSIONS

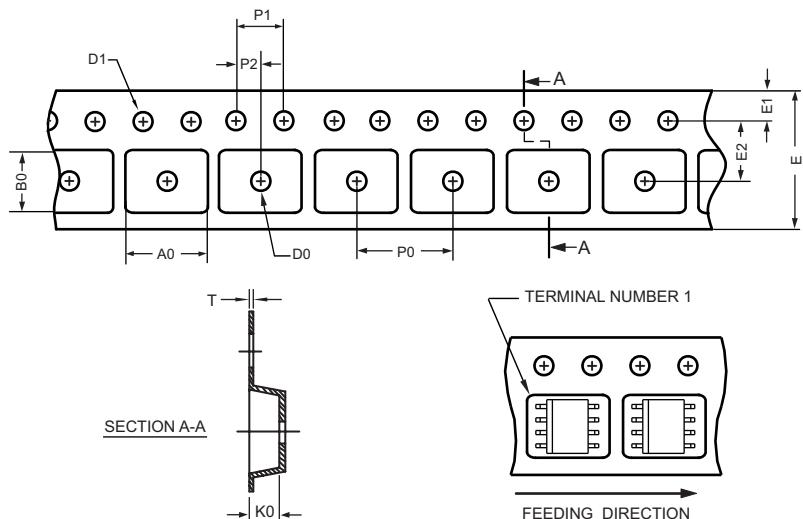
SO-8



| SYMBOLS | MILLIMETERS | | INCHES | |
|---------|-------------|------|--------|-------|
| | MIN | MAX | MIN | MAX |
| A | 1.35 | 1.75 | 0.053 | 0.069 |
| A1 | 0.10 | 0.25 | 0.004 | 0.010 |
| D | 4.80 | 4.98 | 0.189 | 0.196 |
| E | 3.81 | 3.99 | 0.150 | 0.157 |
| H | 5.79 | 6.20 | 0.228 | 0.244 |
| L | 0.41 | 1.27 | 0.016 | 0.050 |
| θ | 0° | 8° | 0° | 8° |

SO-8 Tape and Reel Data

SO-8 Carrier Tape



unit:mm

| PACKAGE | A0 | B0 | K0 | D0 | D1 | E | E1 | E2 | P0 | P1 | P2 | T |
|------------------|--------------------|--------------------|--------------------|---------------------|---------------------------|--------------------------|--------------------|-------------------|-------------------|-------------------|-------------------|---------------------|
| SOP 8N 150mil | 6.50 ± 0.15 | 5.25 ± 0.10 | 2.10 ± 0.10 | $\phi 1.5$ (MIN) | $\phi 1.55$ ± 0.10 | 12.0 $+0.3$ -0.1 | 1.75 ± 0.10 | 5.5 ± 0.10 | 8.0 ± 0.10 | 4.0 ± 0.10 | 2.0 ± 0.10 | 0.30 ± 0.013 |

SO-8 Reel

